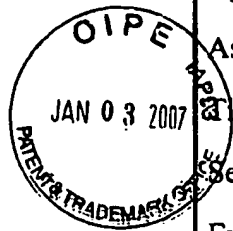


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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Applicant(s): Shu Yuan, et al.
Assignee: Lattice Semiconductor Corporation
Title: Fast Diagonal Interleaved Parity (DIP) Calculator
Serial No.: 10/791,073 Filing Date: March 1, 2004
Examiner: Baker, Stephen M. Group Art 2133
Unit:
Docket No.: M-15301 US Confirmation 2971

Irvine, California
December 28, 2006

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RULE 312 AMENDMENT

Dear Sir:

In response to the Notice of Allowance dated October 6, 2006, Applicants submit the following amendment and remarks.

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